

Important Dates:

- Last Date: January 15,2022
- Notification: January 30,2022
- Registration: February 10,2022
- CRC Copy: February 10, 2022

Publication

All accepted and registered papers of this special session will be submitted for inclusion into IEEE Xplore.

IEEE Xplore Digital Library. IEEE
Conference Record # 53509

Presence on IEEE Website

<https://tinyurl.com/bsbmbv63>

Submission Guidelines

As per IEEE Guidelines

Previous Conferences

ESCI-2021

<https://tinyurl.com/pws2s5m8>

ESCI-2020

<https://tinyurl.com/d5t99emd>

WIECON-ECE 2016

<https://tinyurl.com/3pt23mb9>

Registration

Please visit conference webpage for registration and other details:

<https://esciioit.org/>

Special discount of Rs. 1000/- on registration fees for all accepted papers of this special session.

Call for Papers

Session Title: Advanced Technologies for Next Generation Electronic Devices, Circuits and Systems

Session Chair:

Dr. Suhaib Ahmed, Baba Ghulam Shah Badshah University, Rajouri, Jammu & Kashmir, India

Dr. Sparsh Sharma, National Institute of Technology, Srinagar, Jammu & Kashmir, India.

Abstract: The existing CMOS technology is expected to remain dominant for the next decade, however, various technologies are being explored to address the shortcomings faced by CMOS technology in the nano scale. Since these technologies are new and in the premature phase a lot of scope lies ahead of researchers to take the designing using these technologies to a commercial level. This Special Issue aims to solicit original research papers, as well as review articles, with a focus to explore emerging technologies being research for low power, high performance integrated circuits along with various theoretical approaches, numerical simulations and experimental studies on non-conventional devices being explored for various novel applications.

Topics of Interest: Original and unpublished papers are invited in the following topics and not limited to:

- Digital Circuit Designing in Quantum dot Cellular Automata Technology
- Fault Tolerant Designing and Testing of Circuits
- Low Power Analog, Digital and Mixed-Signal Circuits and Systems
- Circuits and Systems for IoT devices and AI systems
- Circuits and Systems for Biomedical, Communication, Image and Video Processing Applications
- Beyond CMOS: Nanoelectronics and Hybrid Systems Integration
- Semiconductor Device Modelling
- Design of Memories

Paper Submission Process: Please submit your paper (in word/pdf format) at email: sabatt@ieee.org, with Name of Special Session mentioned in the subject line.

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